

Curiosity_00 (Debugging) - Microchip Studio Advanced Mode Quick Launch (Ctrl+Q)

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Debug Browser CCP AVR128DA48 UPDI on nEDBG (MCHP3280031800001580)

Curiosity_00 main.asm

```

STD    Z+BAUDH,R17    ; store in BAUDH
STD    Z+BAUDL,R16    ; store in BAUDL

POP    R17

; Set USART operating mode

LDI    R16,$03        ; ASYNC operation, no parity, 1 stop, 8 data
STD    Z+USART_CTRLC,R16 ;

LDI    R16,$80        ; enable RX complete interrupt
STD    Z+USART_CTRLA,R16 ; (TX int disabled for now)

LDI    R16,$00        ; no autobaud functions
STD    Z+USART_CTRLD,R16 ;

; Enable RX & TX

LDI    R16,$C0        ; enable RX & TX operation, normal mode
STD    Z+USART_CTRLB,R16 ;

```

Processor Status

Name	Value
Program Counter	0x00000000
Stack Pointer	0x00000000
X Register	0x00000000
Y Register	0x00000000
Z Register	0x00000000
Status Register	0x00000001
Cycle Counter	0
Frequency	
Stop Watch	

Registers

Register	Name	Address	Value	Bits
R00			0x00	
R01			0x00	
R02			0x00	
R03			0x00	
R04			0x00	
R05			0x00	
R06			0x00	
R07			0x00	
R08			0x00	
R09			0x00	
R10			0x00	
R11			0x00	
R12			0x00	

I/O

Name	Address	Value	Bits
RXDATAL	0x820	0x00	
RXDATAH	0x821	0x00	
TXDATAH	0x822	0x3E	
TXDATAH	0x823	0x00	
STATUS	0x824	0x60	
BDF		0x00	
DREIF		0x01	
ISFIF		0x00	
RXCIF		0x00	
RXSIF		0x00	
TXCIF		0x01	
WFB		0x00	
CTRLA	0x825	0x80	
CTRLB	0x826	0xC0	
CTRLC	0x827	0x03	
BAUD	0x828	0x0341	
CTRLD	0x82A	0x00	
DBGCTRL	0x82B	0x00	
EVCTRL	0x82C	0x00	
TXPLCTRL	0x82D	0x00	
RXPLCTRL	0x82E	0x00	

Memory 4

Address	Value
0x0000	?? ?
0x0012	?? ?
0x0024	?? ?
0x0036	?? ?

Autos

Name	Value	Type

Ready