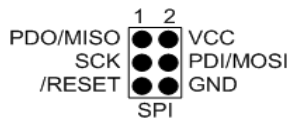
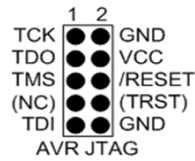
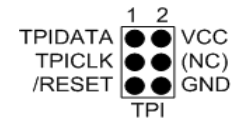
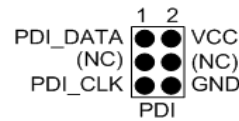


AVR Port Pins		AVR JTAG Header		SPI & DW Header *		PDI Header		TPI Header		aWire Header	
PIN	SIGNAL	Target PIN	Target SIGNAL	Target PIN	Target SIGNAL	Target PIN	Target SIGNAL	Target PIN	Target SIGNAL	Target PIN	Target SIGNAL
Pin 1	TCK	1	TCK	3	SCK			3	TPICLK		
Pin 2	GND	2	GND	6	GND	6	GND	6	GND	6	GND
Pin 3	TDO	3	TDO	1	MISO	1	PDI_DATA	1	TPIDATA	1	(!Reset)DATA
Pin 4	VTG/VCC	4	VCC	2	VCC	2	VCC	2	VCC	2	VCC
Pin 5	TMS	5	TMS								
Pin 6	ISRST	6	!RESET	5	!RESET	5	PDI_CLK	5	!RESET		
Pin 7	n/c	7	n/c								
Pin 8	!TRST	8	!TRST								
Pin 9	TDI	9	TDI	4	MOSI						
Pin 10	GND	10	GND								



*SPI & debugWIRE share the same wiring



SAM Port Pins		SAM JTAG Header		SAM SWD Header	
PIN	SIGNAL	Target PIN	Target SIGNAL	Target PIN	Target SIGNAL
Pin 1	VTG/VCC	1	VCC	1	VCC
Pin 2	TMS	2	TMS	2	SWDIO
Pin 3	GND	3	GND	3	GND
Pin 4	TCK	4	TCK	4	SDCLK
Pin 5	GND	5	GND	5	GND
Pin 6	TDO	6	TDO	6	SWO
Pin 7	!TRST	7	(KEY)	7	(KEY)
Pin 8	TDI	8	TDI	8	n/c
Pin 9	GND	9	GND	9	GND
Pin 10	ISRST	10	!RESET	10	!RESET

Key - Do Not Connect; only used on some AVR devices

! - Indicates inverted signal (Not)

