

## 2.4 Voltage References

The application can choose between the following voltage references for conversions:

- Bandgap Reference (1.1V) — **Internal 1.00v**
- Analog voltage supply ( $V_{DD}$ )
- External reference ( $V_{REF+}$ )

Note that the external reference pin  $V_{REF+}$  is shared with the ADC module. The voltage reference is select with the *Reference* bitfield ( $REFSEL$ ) in *Control Register C* (CTRLB). — **CTRLC**

## 2.5 Driving Strength